



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

RODGERS et al.

Serial No.: 10/099,724

Filed: March 15, 2002

Confirmation No.: 1885

Atty. File No.: 50060-00051

For: "SHIELDED MULTI-CONDUCTOR
INTERCONNECT BUS FOR MEMS"

) Group Art Unit: 2827
)
) Examiner: Dinh, Tuan T.

) SUBMISSION OF FORMAL DRAWINGS
)

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| <p>CERTIFICATE OF MAILING</p> <p>I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE AS FIRST CLASS MAIL IN AN ENVELOPE ADDRESSED TO COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450 ON <u>MARCH 2, 2004</u>.</p> <p>MARSH FISCHMANN & BREYFOGLE LLP</p> <p>BY: <u>[Signature]</u> DAXMARA SANCHEZ</p> |
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Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the "Notice of Allowability" accompanying the "Notice of Allowance and Issue Fee Due" mailed December 5, 2003, regarding the above-identified patent application, enclosed for filing with said application are 16 sheets of formal, inked drawings illustrating Figs. 1A through 7 of the application. Figs. 1A through 7 constitute all of the drawings in the application.

Respectfully submitted,

MARSH FISCHMANN & BREYFOGLE LLP

By [Signature]
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Date: March 2, 2004